

What is claimed is:

1. A display device being characterized in that gate signal lines which are extended in the x direction and are arranged in parallel in the y direction, scanning signal driving circuits which supply scanning signals to respective gate signal lines, drain signal lines which are extended in the y direction and are arranged in parallel in the x direction, and video signal driving circuits which supply video signals to respective drain signal lines are formed on one surface of an insulating substrate,

the display device includes a thin film transistor which is driven by the scanning signals from one side of the gate signal line and a pixel electrode to which the video signals from one side drain of the signal line are supplied through this thin film transistor in each pixel region which is surrounded by the respective signal lines,

the display region which is a collection of the pixel regions is divided into two separate display regions using an imaginary line extending along the x direction as a boundary,

the scanning signal driving circuit which supplies the scanning signals to respective gate signal lines in one display region and the scanning signal driving circuit which supplies the scanning signals to respective gate signal lines in the other display region are separately formed,



the drain signal lines at one display region side are separated from the drain signal lines at the other display region, and

the video signal driving circuit which supplies the video signals to respective drain signal lines in one display region and the video signal driving circuit which supplies the video signals to respective drain signal lines in the other display region are separately formed.

2.A display device according to claim 1, wherein the display device is provided with power supply changeover means which drives the scanning signal driving circuit and the video signal driving circuit at one display region side and the scanning signal driving circuit and the video signal driving circuit at the other display region side together or drives the scanning signal driving circuit and the video signal driving circuit at only one of both display regions.

3.A display device according to claim 1, wherein the area which divides respective drain signal lines of one display region side and respective drain signal lines of the other display region side is positioned over the gate signal line which is arranged over the drain signal line and an intervening insulation film, and separated end portions of respective drain signal lines at one display region side and separated end portions of respective drain signal lines at the other display region side are all superposed on the gate signal lines.

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4. A display device according to claim 1, wherein the scanning signals are supplied to the gate signal lines sequentially in the direction moving away from respective gate signal lines at the boundary of one display region and the other display region, and the video signals are supplied from the video signal driving circuit in synchronism with the supply of the scanning signals.

5. A display device according to claim 1, wherein the scanning signals are supplied to the respective gate signal lines sequentially in the direction approaching the gate signal lines at the boundary between one display region and the other display region from the gate signal lines which are present at the respective sides of one display region and the other display region which are remote from the boundary, and the video signals are supplied from the video signal driving circuit in synchronism with the supply of the scanning signals.

6. A display device being characterized in that gate signal lines which are extended in the x direction and are arranged in parallel in the y direction, a scanning signal driving circuit which supplies scanning signals to respective gate signal lines, drain signal lines which are extended in the y direction and are arranged in parallel in the x direction, and a video signal driving circuit which supplies video signals to respective drain signal lines are formed on one surface of an insulating substrate,

the display device includes a thin film transistor which is driven by the scanning signals from one side of the gate signal line and a pixel electrode to which the video signals from one side of the drain signal line are supplied through this thin film transistor in each pixel region which is surrounded by the respective signal lines,

the video signal driving circuit includes a dynamic memory which is comprised of a plurality of other thin film transistors formed in parallel with the above thin film transistor, and

at least one thin film transistor among this plurality of other thin film transistors is covered with a conductive film having a potential which is fixed through an insulation film.

7. A display device according to claim 6, wherein conductive film is formed of material equal to material of the pixel electrodes.

8. A display device being characterized in that the display device comprises a liquid crystal display panel and a backlight which is arranged at a back surface of the liquid crystal display panel,

the liquid crystal display panel includes gate signal lines which are extended in the x direction and are arranged in parallel in the y direction, a scanning signal driving circuit which supplies scanning signals to respective gate signal lines, drain signal lines which are extended in the y

direction and are arranged in parallel in the x direction, and a video signal driving circuit which supplies video signals to respective drain signal lines on one of the substrates which are arranged to face each other with liquid crystal inserted between them, on the side facing the liquid crystal.

the display device includes a thin film transistor which is driven by the scanning signals from one side of the gate signal line and a pixel electrode to which the video signals from one side of the drain signal line are supplied through the thin film transistor in each pixel region which is surrounded by the respective signal lines,

the video signal driving circuit includes a dynamic memory which is comprised of a plurality of other thin film transistors formed in parallel with the above-mentioned thin film transistor, and

a light shielding film which is capable of preventing light from the backlight from being irradiated to the dynamic memory is formed on the substrate at the side which faces the backlight.

9. A display device according to claim 8, wherein the dynamic memory is formed on the substrate facing the backlight and the light shielding film is formed on a portion which faces the dynamic memory with the substrate intervening between them.